

Attorney Docket No.: 5649-968

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Name: Lee et al.

Serial No.: 10/046,805

Filed: January 14, 2002

For: METHODS OF FORMING METAL LAYERS IN INTEGRATED CIRCUIT
DEVICES USING SELECTIVE DEPOSITION ON EDGES OF RECESSES AND
CONDUCTIVE CONTACTS SO FORMED

January 9, 2004

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §1.97(c)**

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document.

This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(c), before final Office Action or Allowance, whichever is earlier.

In accordance with the requirements of 37 C.F.R. § 1.97(c)(2), we submit the \$180.00 fee specified in 37 C.F.R. § 1.17(p) by authorizing the Commissioner to charge the fee to Deposit Account No. 50-0220. This amount is believed to be correct. However, the Commissioner is also authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

Robert N. Crouse
Registration No. 44,635

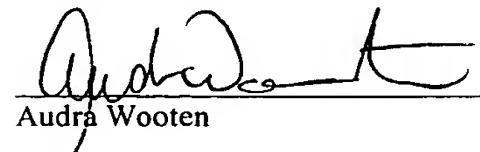
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Page 2

Certificate of Mailing under 37 CFR 1.8 (or 1.10)

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 9, 2004.



Audra Wooten

EXAMINER
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DATE CONSIDERED

Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.